

WEST Search History

Hide Items

Restore

Clear

Cancel

DATE: Tuesday, February 01, 2005

09/825,654

Hide? Set Name Query

Hit Count

DB=USPT; PLUR=YES; OP=OR

<input type="checkbox"/>	L118	5367693.pn.	1
<input type="checkbox"/>	L117	5394551.pn.	1

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<input type="checkbox"/>	L116	semaphore\$1 and L115	23
<input type="checkbox"/>	L115	lock\$3 same l113	260
<input type="checkbox"/>	L114	l110 and L113	36
<input type="checkbox"/>	L113	interrupt\$1 same register\$1 same interface\$1	6386
<input type="checkbox"/>	L112	interrupt adj register adj control adj interface\$1	0
<input type="checkbox"/>	L111	interface\$1 same L110	5
<input type="checkbox"/>	L110	interrupt adj service adj register\$1	63
<input type="checkbox"/>	L109	l24 and l106	3
<input type="checkbox"/>	L108	l90 and l106	42
<input type="checkbox"/>	L107	l93 and L106	0
<input type="checkbox"/>	L106	714/39.ccls.	377
<input type="checkbox"/>	L105	l93 and L104	12
<input type="checkbox"/>	L104	l101 and l103	359
<input type="checkbox"/>	L103	register adj interface	1845
<input type="checkbox"/>	L102	l93 same L101	5
<input type="checkbox"/>	L101	dac or (digital same analog same converter\$1)	217122

DB=USPT; PLUR=YES; OP=OR

<input type="checkbox"/>	L100	5931948.pn.	1
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DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<input type="checkbox"/>	L99	l90 and L98	27
<input type="checkbox"/>	L98	l70 and L97	64
<input type="checkbox"/>	L97	714/25-57.ccls.	9250
<input type="checkbox"/>	L96	l88 and L95	0
<input type="checkbox"/>	L95	l72 and L94	134
<input type="checkbox"/>	L94	interface\$1 same L93	134
<input type="checkbox"/>	L93	access adj control adj register\$1	577
<input type="checkbox"/>	L92	l19 and L91	13
<input type="checkbox"/>	L91	l72 same L90	971
<input type="checkbox"/>	L90	access adj control	51802
<input type="checkbox"/>	L89	l87 and L88	2

INVENTOR NAME
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<input checked="" type="checkbox"/>	L88	l18 or l41 or l42	4788
<input type="checkbox"/>	L87	(control same lock\$3).ti.	16894
<input type="checkbox"/>	L86	l84 and L85	0
<input type="checkbox"/>	L85	latch\$3.ti.	64524
<input type="checkbox"/>	L84	l72 and L83	139
<input type="checkbox"/>	L83	l66 same address\$2	1655
<input type="checkbox"/>	L82	L81 same l72	8
<input type="checkbox"/>	L81	software adj lock\$3	563

DB=USPT; PLUR=YES; OP=OR

<input type="checkbox"/>	L80	US-5630102-A.did.	1
<input type="checkbox"/>	L79	5440747.pn.	1
<input type="checkbox"/>	L78	US-5630102-A.did.	1
<input type="checkbox"/>	L77	5515530.pn.	1
<input type="checkbox"/>	L76	US-5630102-A.did.	1
<input type="checkbox"/>	L75	US-5630102-A.did.	1

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<input type="checkbox"/>	L74	L73.ti.	5
<input type="checkbox"/>	L73	lock\$3 same L72	1119
<input type="checkbox"/>	L72	control adj register\$1	24059
<input type="checkbox"/>	L71	l66 and L70	91
<input type="checkbox"/>	L70	register\$1 same lock\$3 same access	5822
<input type="checkbox"/>	L69	hardware same l66	631
<input type="checkbox"/>	L68	l65 and l66	0
<input type="checkbox"/>	L67	l65 same L66	0
<input type="checkbox"/>	L66	control adj point\$1	27205
<input type="checkbox"/>	L65	wrapper\$1 same lock\$1	1106
<input type="checkbox"/>	L64	L58 and L63	0
<input type="checkbox"/>	L63	latch same lock\$3 same object\$1	27112
<input type="checkbox"/>	L62	L47 and L61	0
<input type="checkbox"/>	L61	object\$1 and L60	50
<input type="checkbox"/>	L60	L58 and L55	62
<input type="checkbox"/>	L59	L57 and L58	0
<input type="checkbox"/>	L58	L18 or L41 or L42 or L38	5524
<input type="checkbox"/>	L57	L55 and L5	3
<input type="checkbox"/>	L56	L54 and L55	125874
<input type="checkbox"/>	L55	lock\$3 adj mechanism\$1	125877
<input type="checkbox"/>	L54	lock\$3 same mechanism	381319

DB=USPT; PLUR=YES; OP=OR

<input type="checkbox"/>	L53	5862052.pn.	1
<input type="checkbox"/>	L52	5916306.pn.	1

<input type="checkbox"/>	L51	5918233.pn.	1
<input type="checkbox"/>	L50	6028998.pn.	1
<input type="checkbox"/>	L49	6028998.pn.	1
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L48	L45 and L46 and L47	2
<input type="checkbox"/>	L47	latch adj object\$1	168
<input type="checkbox"/>	L46	access adj object\$1	5743
<input type="checkbox"/>	L45	hardware adj2 object\$1	706
<input type="checkbox"/>	L44	L23 and L43	0
<input type="checkbox"/>	L43	L41 and L42 and L38 and L18	0
<input type="checkbox"/>	L42	(713/100).cccls.	867
<input type="checkbox"/>	L41	(713/1 713/2).cccls.	2427
<input type="checkbox"/>	L40	orchestration adj object\$1	1
<input type="checkbox"/>	L39	L23 and L38	15
<input type="checkbox"/>	L38	(719/328).cccls.	876
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L37	6480597.pn.	1
<input type="checkbox"/>	L36	5325532.pn.	1
<input type="checkbox"/>	L35	5691897.pn.	1
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L34	L19 and L32	0
<input type="checkbox"/>	L33	L23 and L32	0
<input type="checkbox"/>	L32	control same latch same (layer\$1 and object\$1)	314
<input type="checkbox"/>	L31	L23 and L30	0
<input type="checkbox"/>	L30	control same latch same (layer\$1 or object\$1)	17929
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L29	5987614.pn.	1
<input type="checkbox"/>	L28	6185514.pn.	1
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L27	L23.ti.	2
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L26	L18 and L25	6
<input type="checkbox"/>	L25	hal and L23	165
<input type="checkbox"/>	L24	hal or L23	17153
<input type="checkbox"/>	L23	Hardware adj abstraction adj layer\$1	260
<input type="checkbox"/>	L22	wrapper\$1 and L21	0
<input type="checkbox"/>	L21	firmware same L19	28
<input type="checkbox"/>	L20	L5 and L19	0
<input type="checkbox"/>	L19	control adj points	11245
<input type="checkbox"/>	L18	L7 or L8 or L9 or L10 or L11 or L12 or L13 or L14 or L15 or L16 or L17	1967
	L17	(719/320).cccls.	85



<input type="checkbox"/>	L16	(719/319).ccls.	65
<input type="checkbox"/>	L15	(719/318).ccls.	269
<input type="checkbox"/>	L14	(719/317).ccls.	127
<input type="checkbox"/>	L13	(719/316).ccls.	279
<input type="checkbox"/>	L12	(719/315).ccls.	563
<input type="checkbox"/>	L11	(719/314).ccls.	92
<input type="checkbox"/>	L10	(719/313).ccls.	268
<input type="checkbox"/>	L9	(719/312).ccls.	102
<input type="checkbox"/>	L8	(719/311).ccls.	69
<input type="checkbox"/>	L7	(719/310).ccls.	383
<input type="checkbox"/>	L6	L5.ti.	1
<input type="checkbox"/>	L5	latch adj object\$1	41
<input type="checkbox"/>	L4	L1 and L2	0
<input type="checkbox"/>	L3	L1 and L2L2	0
<input type="checkbox"/>	L2	hardware adj control adj object\$1	1
<input type="checkbox"/>	L1	latch adj layer\$1	9

END OF SEARCH HISTORY